



**ATTORNEY DOCKET NO. 43889-861**  
**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**In re Application of:**

Yutaka TERADA, et al.

**Serial No.: 09/299,659**

**Filed: April 27, 1999**

For: INPUT CIRCUIT AND OUTPUT CIRCUIT)

Group Art Unit: 2634

**Examiner: Chieh M. FAN**

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**ELECTION UNDER 35 U.S.C. § 121** Technology Center 2600  
Technology Center 2600

Hon. Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

In response to the restriction requirement set forth in the Office Action mailed July 2, 2002, having a shortened statutory period for response set to expire August 2, 2002, wherein the Examiner required restriction between Group I - claims 1-5, drawn to an input circuit and Group II - claims 6 and 7, drawn to an output circuit, Applicants elect without traverse Group I, claims 1-5 for initial prosecution on the merits.

**Please cancel claims 6 and 7 without prejudice.**

Applicants also reserve the right to file a Divisional Application for the non-elected claims 6 and 7, which the Examiner has indicated is patentably distinct.

Applicants believe that no extension of time is required. However, this conditional petition is being made to provide for the possibility that Applicants have inadvertently overlooked the need for a petition for extension of time. The


Serial No.: 09/299,659

Commissioner is hereby authorized to charge any additional fees associated with this communication or credit any overpayment to Deposit Account No. 50-0417.

Respectfully submitted,

McDERMOTT, WILL & EMERY

Date: 8/2/02

By:   
Michael E. Fogarty  
Registration No. 36,139

600 13th Street, N.W. , Suite 1200  
Washington, D.C. 20005-3096  
Telephone: 202-756-8000  
Facsimile: 202-756-8087

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